Clock kit instructions

Operation instruction

It will display 12:59 when Power-on, while is normal interface("hours:minutes"). The both channels of alarm clock are opened.At the same time,the first alarm clock has been set at 13:01.the second alarm clock has been set at 13:02. After power on ,short press S2.The display of digital tube will switch between "hours:minutes" and "minutes:seconds";Long press S1 to enter the system Settings menu. there are A, B, C, D, E, F, G, H, I submenu. Short press S1 sub-menu plus increase by degrees。finally back to the normal interface

A Sub menu : Correction for hours

Display data will add 1 after press S2.after adjusted the A Submenu,then short press S2 to save the adjusted results and quit A submenu,enter B sbumenu

B Sub menu : Correction for minutes

Display data will add 1 after press S2.after adjusted the B Submenu,then short press S2 to save the adjusted results and quit B submenu,enter C sbumenu

C Sub menu:on time alarm switch

The default state is ON (on-time-alarm is open from 8:00 to 20:00)
It will switch between ON and OFF(on-time-alarm is closed) when press S2.
Short press S2 to save the adjusted results and quit C submenu,enterD sbumenu

D Sub menu:The first alarm-clock switch

The default state is ON (the first alarm-clock is opened)
It will switch between ON and OFF(first-alarm-clock is closed) when press S2。
If set to ON, short press S1 to save and quit,then enter E submenu;
If set to OFF, short press S1 to save and quit ,then enter G submenu;

E Sub menu:The first alarm clock set for hours

Display data will add 1 after press S2.after adjusted the E Submenu,then short

press S2 to save the adjusted results and quit E submenu,enter F sbumenu

F Sub menu:The first alarm clock set for minutes

Display data will add 1 after press S2.after adjusted the F Submenu,then short press S2 to save the adjusted results and quit F submenu,enter G sbumenu

G Sub menu:The Second alarm-clock switch

The default state is ON (the second alarm-clock is opened)
It will switch between ON and OFF(second-alarm-clock is closed) when press S2。
If set to ON, short press S1 to save and quit ， then enter H submenu;
If set to OFF, short press S1 to save and quit ， then enter normal interface;

H Sub menu:The second alarm clock set for hours

Display data will add 1 after press S2.after adjusted the F Submenu,then short press S2 to save the adjusted results and quit H submenu,enter I sbumenu

I Sub menu:The second alarm clock set for hours

Display data will add 1 after press S2.after adjusted the I Submenu,then short press S2 to save the adjusted results and quit H submenu, then enter normal interface.

Second correction:

Short press S2 in the normal interface,then enter "minutes : seconds" interface .Long press S2,make the second zero.Then short press S2 twice enter normal interface

Features:

AT89C2051-based of four electronic clock kit
Kit Model: YSZ-4
Supply voltage: 3V-6V
PCB Size: 51.2mm*53.9mm

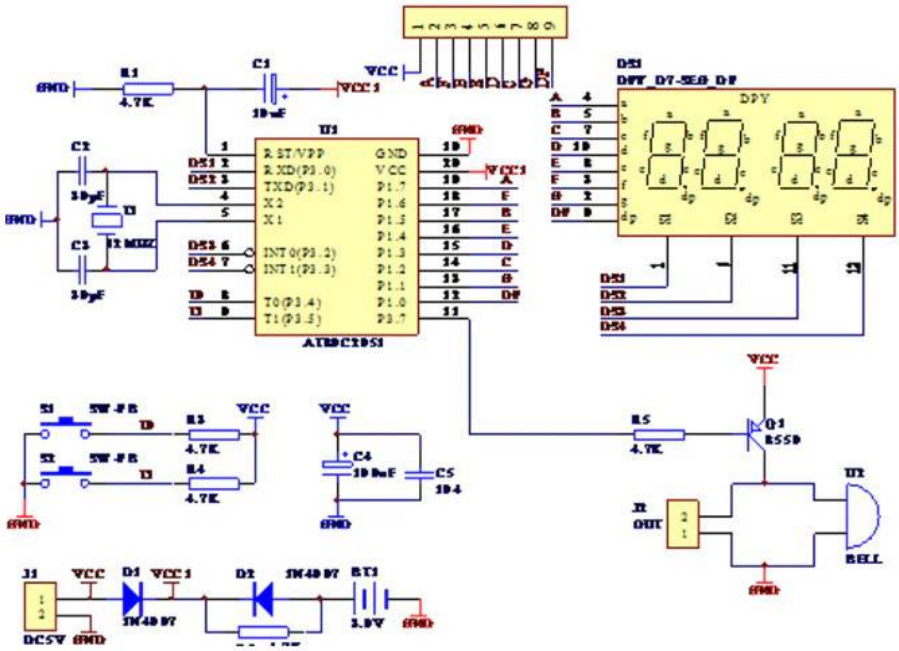
Function:

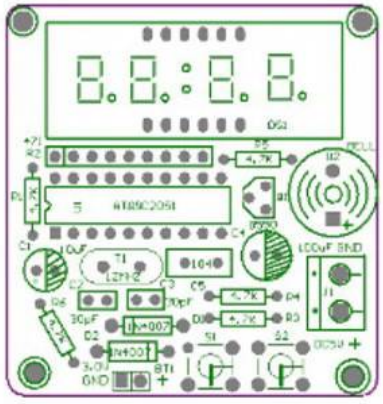
· Seconds correction (for precise School)
· Switch to every minute independent display interface
· Whole point of time (8-20 o\'clock chime can be turned off)
· Two alarm settings (you can turn off the alarm function)

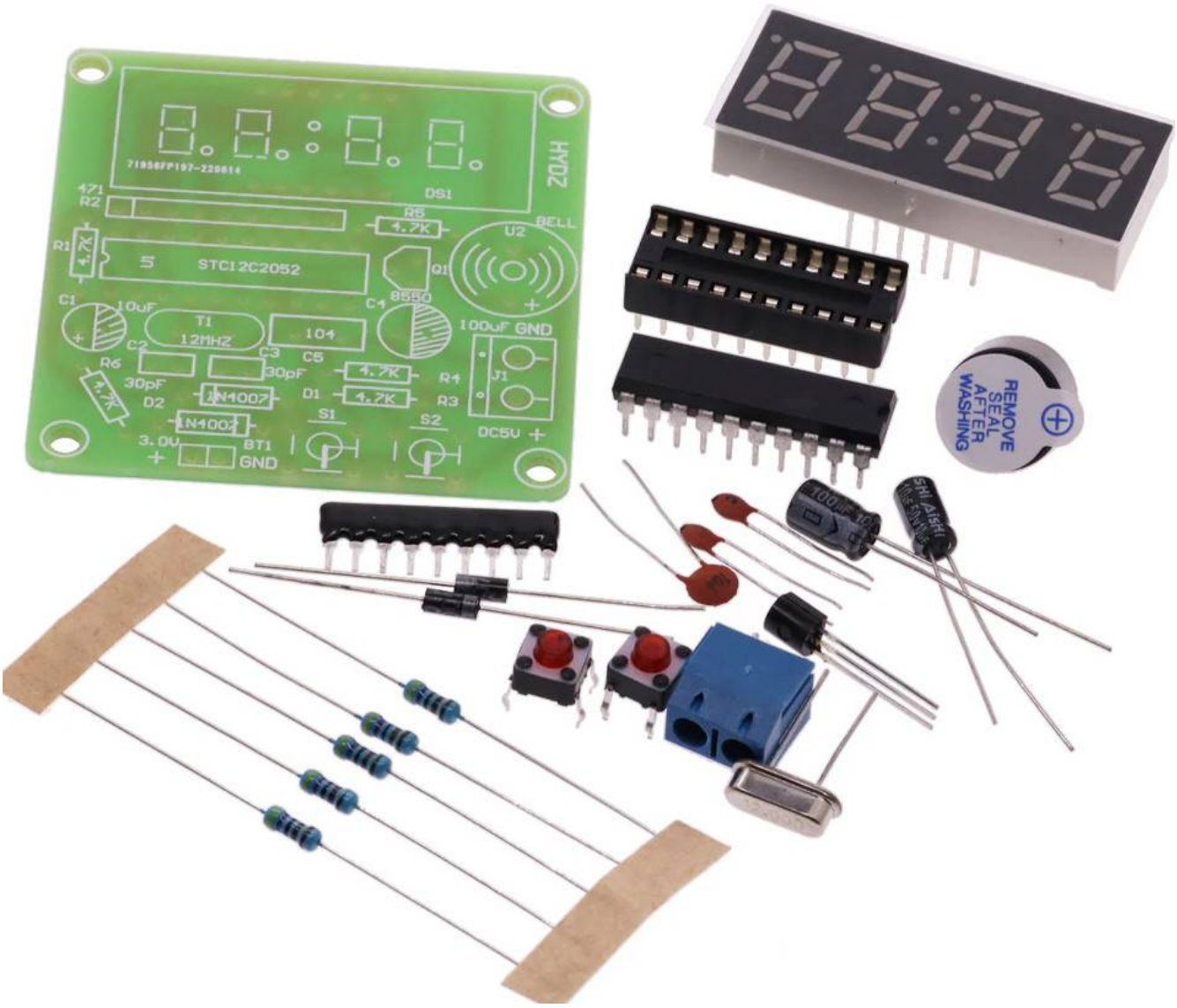
Kit Features:

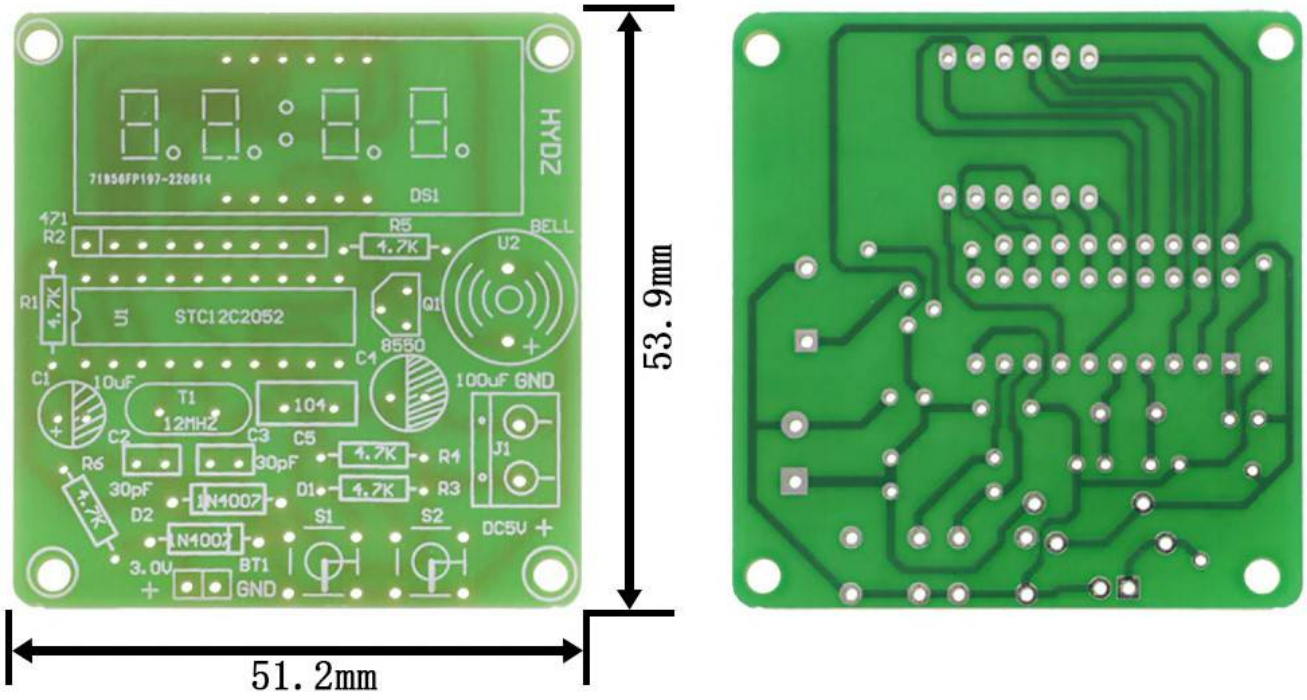
A.0.56 inch special red digital clock for display;
B.Import AT89C2051 for master chip;
C.1.2mm thick PCB made from military grade FR-4 board;
D.Accurate travel time,travel time error range error -1 to +1 seconds every 24 hours.
Setting, LED flashes the last one,the last one by S2 plus one,between 0 and 9 cycles,digital the greater the faster the clock,press S1 to confirm the setting and return to the clock display.Set.

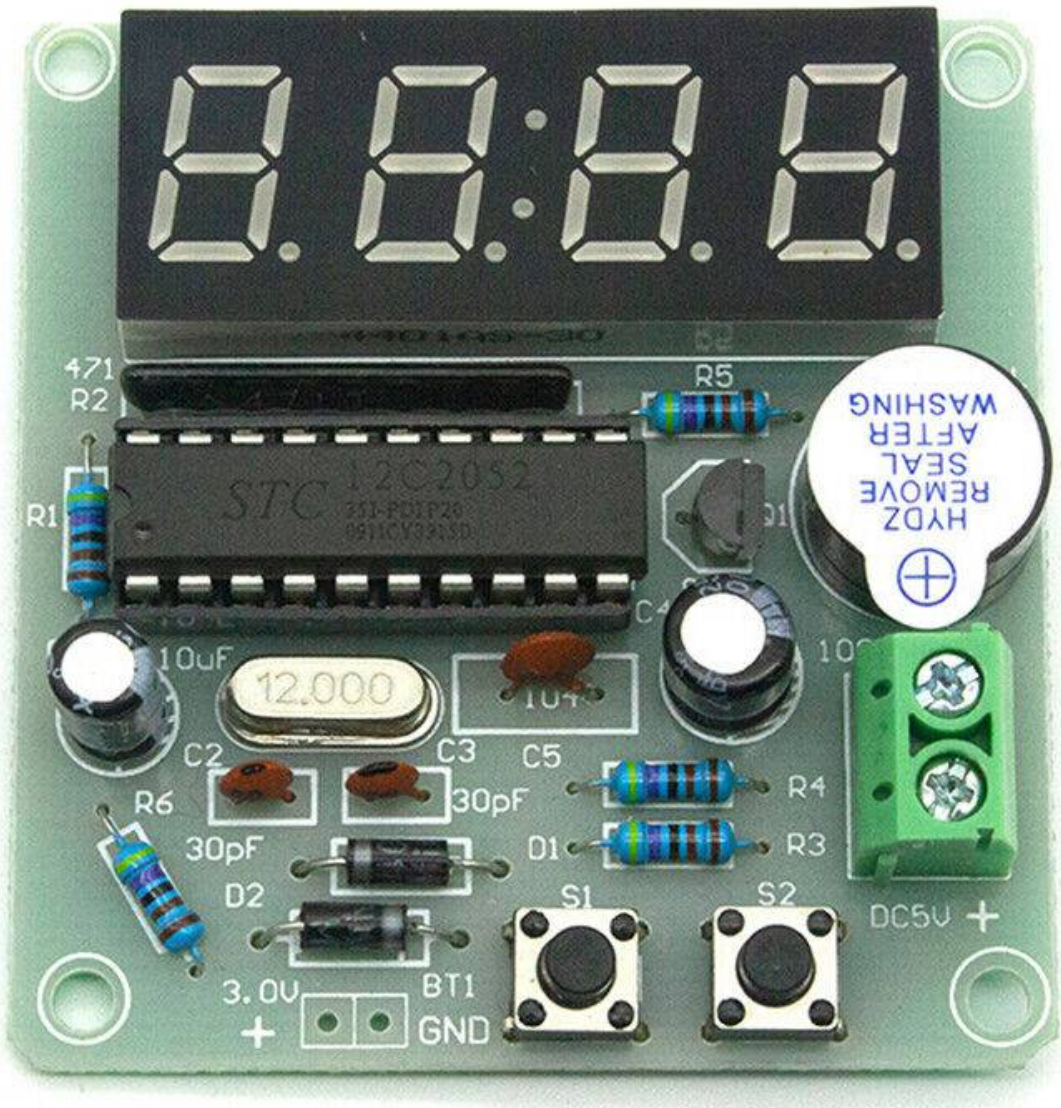
Name	Specification	Quantity	Serial number
0.25w resistance	4.7K	5	R1.R3-R6
Exclusion	470*9	1	R2
Electrolytic capacitor	10uf	1	C1
	100UF	1	C4
Ceramic capacitor	30p	2	C2, C3
	104	1	C5
Diode	IN4007	2	D1, D2
Nixie tube	0.4"	1	DS1
Crystal oscillator	12HMz	1	T1
Key switch	6*6	2	S1, S2
Audion	8550	1	Q1
Single chip	AT89C2051	1	V1
Socket	DIP20	1	
Buzzer	5V	1	V2
Terminal	KF126-2P	1	J1
Circuit board		1	

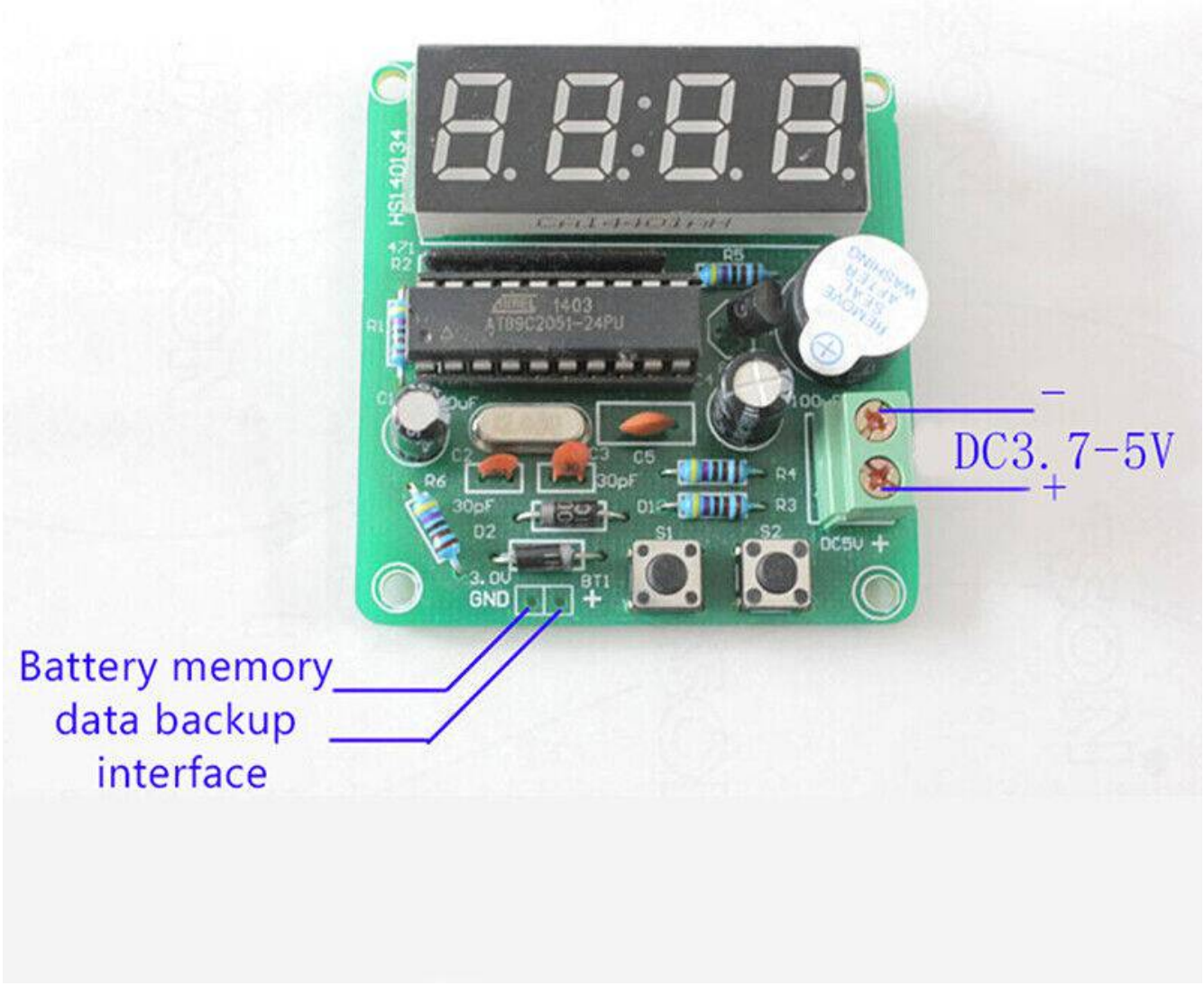

GND
R1
4.7K
C1
10uF
VCC
VCC1
U1
RST/VPP
RXD(P3.0)
TXD(P3.1)
X2
X1
INT0(P3.2)
INT1(P3.3)
T0(P3.4)
T1(P3.5)
GND
VCC
P1.7
P1.6
P1.5
P1.4
P1.3
P1.2
P1.1
P1.0
P3.7
AT89C2051
C2
30pF
C3
30pF
Y1
12 MHz
DS1
DPY_7-SEG_DP
DPY
S1
SW-PB
S2
SW-PB
R3
4.7K
R4
4.7K
C4
10 uF
C5
104
R5
4.7K
Q1
8550
BELL
J1
DC5V
D1
1N4007
D2
1N4007
BT1
3.0V




DS1
R5
4.7K
U2
BELL
R1
4.7K
STC12C2052
Q1
8550
C1
10uF
T1
12MHz
104
C4
100uF GND
C2
C3
C5
30pF
30pF
R6
4.7K
D2
1N4007
D1
4.7K
R4
R3
1N4007
S1
S2
DC5V
3.0V
BT1
GND
J1
REMOVE SEAL AFTER WASHING


53.9mm
51.2mm
HYDZ
DS1
STC12C2052
BELL
U2
Q1
8550
R1
R2
R3
R4
R5
R6
4.7K
C1
10uF
C2
C3
30pF
C5
104
100uF
GND
T1
12MHZ
J1
D1
D2
1N4007
S1
S2
DC5V
3.0V
BT1
GND


8.8:8.8.
471
R2
R5
R1
STC 12C2052
D1
HYDZ
REMOVE
SEAL
AFTER
WASHING
10uF
12.000
104
C2
C3
C5
30pF
R4
R6
30pF
D1
R3
D2
S1
S2
DC5V +
3.0V
BT1
+
GND


HS140134
R5
1403
AT89C2051-24PU
C1
C2
C3
C5
R6
30pF
30pF
R4
R3
D1
D2
S1
S2
DC5V +
3.0V
BT1
GND
+
−
DC3.7-5V
+
Battery memory
data backup
interface